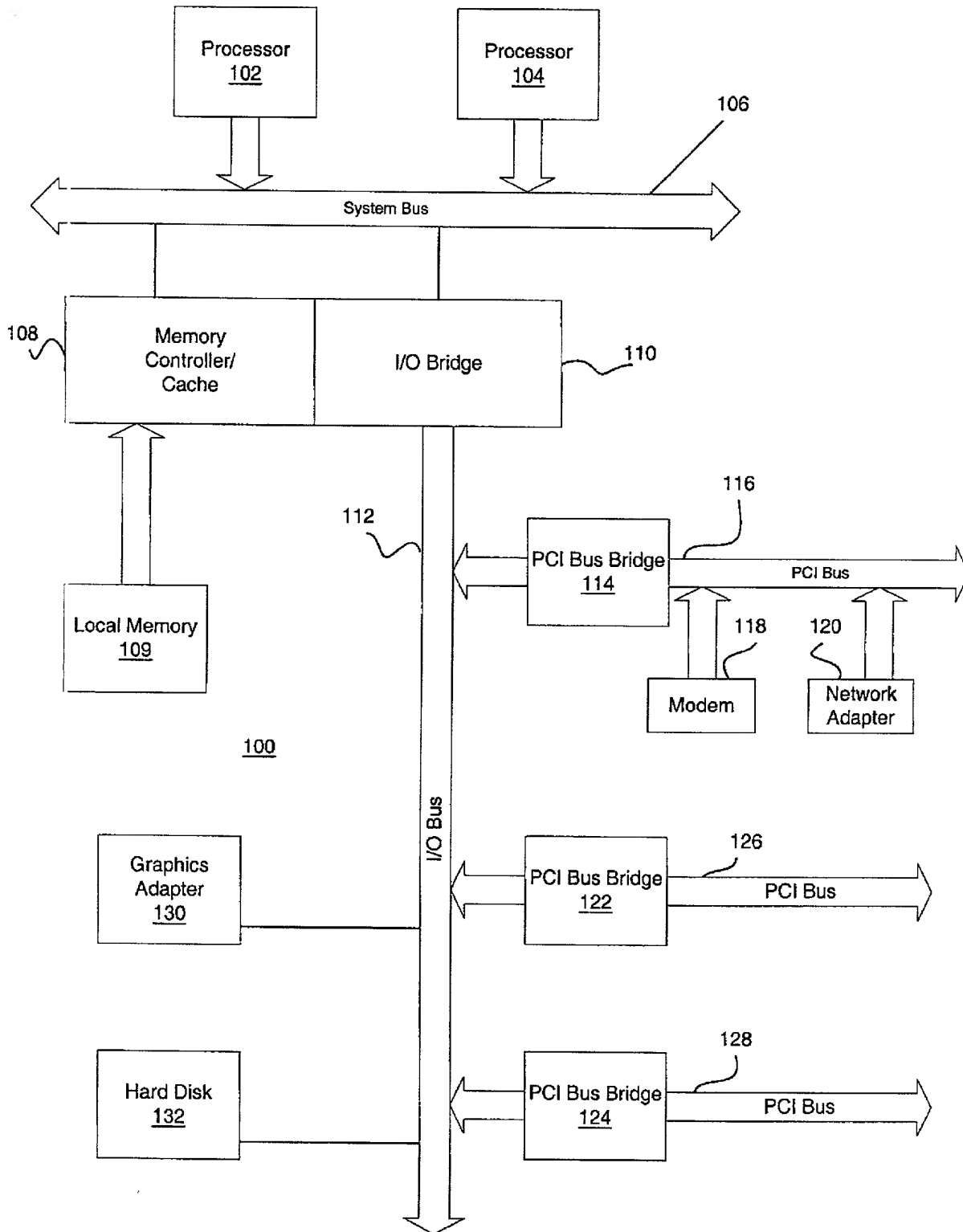


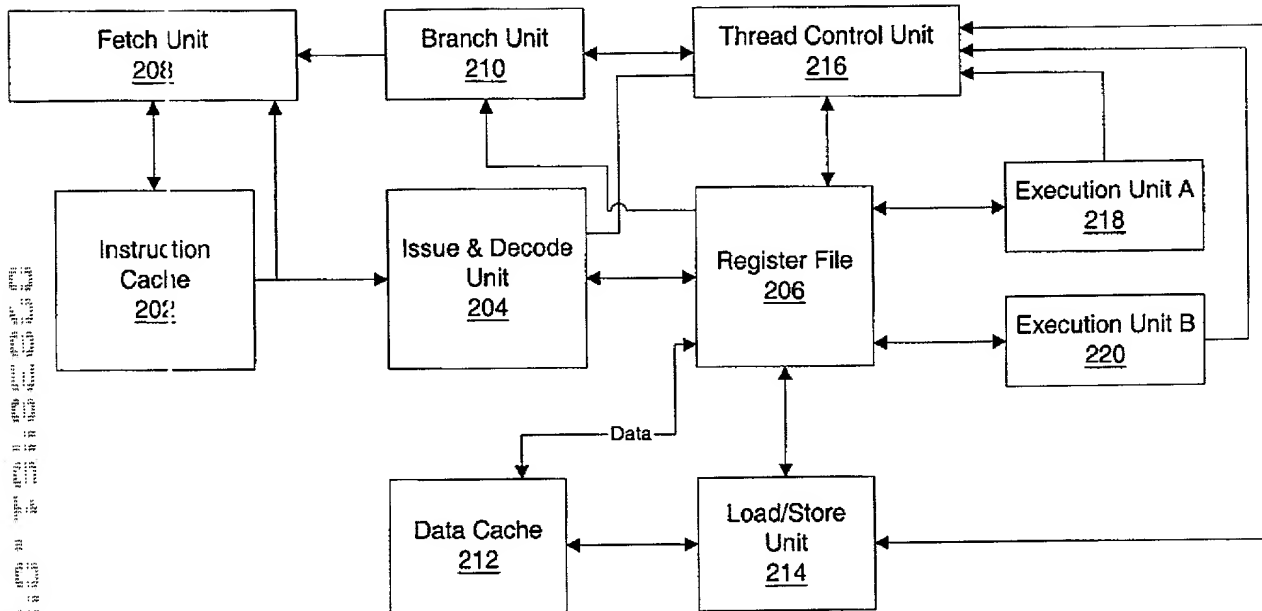
# Figure 1

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# Figure 2

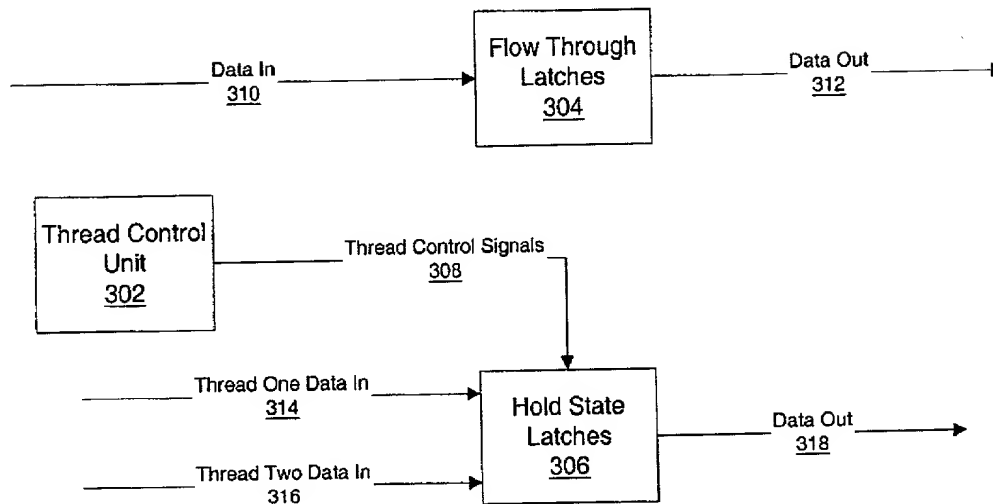
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200  
Processor

# Figure 3

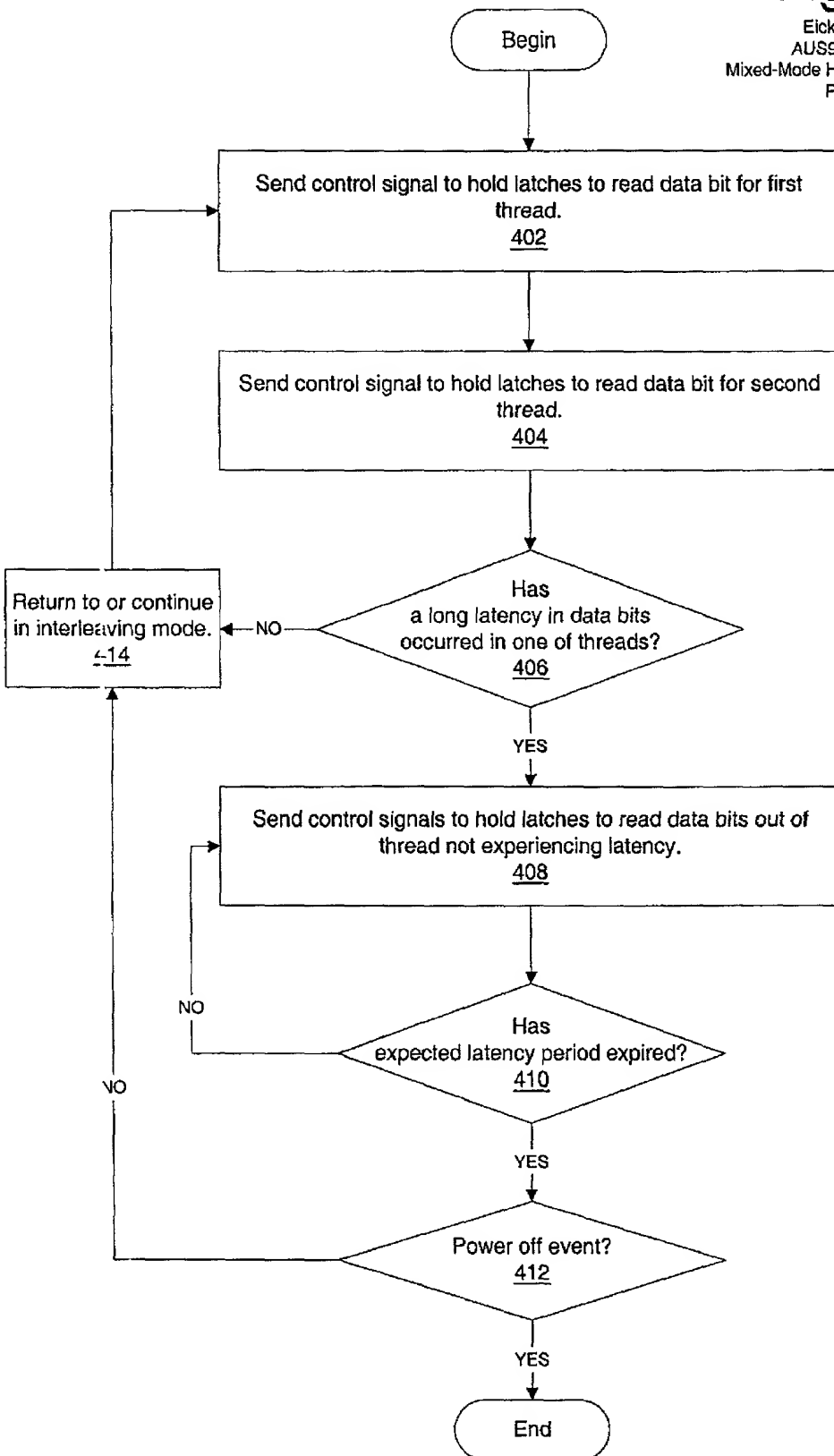
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300  
Thread Control System

# Figure 4

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# Figure 5

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